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INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional)	Application Number
MM43[illegible]	09/740,666
Applicant(s)	
Akira MASHIMO, et al	
Filing Date	**Group Art Unit**
December 19, 2000	

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U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
DV.		4,286,223	08/25/81	SHEARER			

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation YES	Translation NO
DV.		DE 3031342 C1	08/20/80	GERMANY				

OTHER DOCUMENTS *(Including Author, Title, Date, Pertinent Pages, Etc.)*

DV.		Copy of German Office Action from German Patent Office (5 pages) with English translation thereof (3 pages)
DV.		TIETZE, Ulrich and SCHENK, Ghristoph: Semiconductor Circuit Design, 3rd Edition, 1974, page 512

EXAMINER	DATE CONSIDERED
DV	11/23/04

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.